

2819

PATENT  
Docket No.: ACT-320COA

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

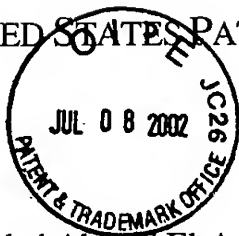
Group Art Unit: 2819

Examiner: Not Yet Assigned

Serial No.: 10/024,661

Filed: December 13, 2001

In re Application of: Khaled Ahmad El-Ayat

For: PROGRAMMABLE MULTI-STANDARD I/O ARCHITECTURE  
FOR FPGAsCertificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail, in an envelope addressed to Director of Patents, Washington, D.C. 20231 on 7-1-02, Signed Caselyn Thompson  
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Sir:

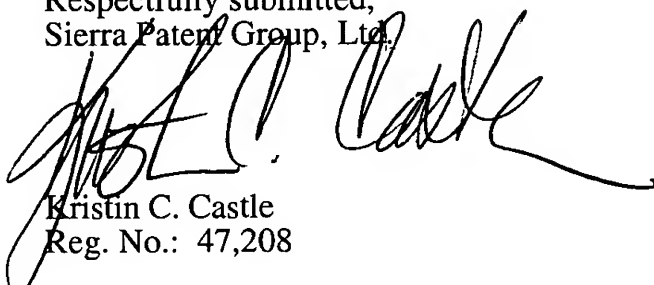
Enclosed please find the following:

1. Information Disclosure Statement;
2. Form PTO-1449
3. Copies of 13 References.

In the event any additional fee is required for filing the above-noted document, including any fees required under 37 CFR 1.136 for any necessary Extension of Time to make the filing of attached document timely, the Assistant Commissioner is hereby authorized to charge the fee to our Deposit Account No.: 50-0612. A duplicate copy of this page is enclosed.

Respectfully submitted,  
Sierra Patent Group, Ltd.

Dated: June 25, 2002

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